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** CONTINUING DATA *****

None

JMJ

** FOREIGN APPLICATIONS *****

ITALY MI2002A002467 11/20/2002

JMJ

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 02/13/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <u>Tracie M. Thomas</u> Examiner's Signature Initials	STATE OR COUNTRY ITALY	SHEETS DRAWING 2	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 2
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TITLE

Process for manufacturing a byte selection transistor for a matrix of non volatile memory cells and corresponding structure

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